

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Makoto FUJIWARA, et al.	:	Confirmation Number: 4108
	:	
Application No.: 10/654,084	:	Group Art Unit: 2137
	:	Allowed: October 15, 2007
Filed: September 04, 2003	:	Examiner: SMITHERS, MATTHEW
	:	

For: SEMICONDUCTOR DEVICE INCLUDING ENCRYPTION SECTION,
SEMICONDUCTOR DEVICE INCLUDING EXTERNAL INTERFACE, AND CONTENT
REPRODUCTION METHOD

312 LETTER RESPONDING TO REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Notice of Allowance/Allowability (NOA) mailed on October 15, 2007 is acknowledged and appreciated. In the Reasons for Allowance included in the NOA, the Examiner suggests that claim 8 recites “wherein the encryption control section includes a mode ID storage register for storing a mode ID; and the encryption control section determines whether to permit execution of each of the sequences based on the value of the mode ID stored in the mode ID storage register.”

To clarify the record, it is noted that independent claim 8 is not limited by the aforementioned feature of the present invention referenced by the Examiner.

Application No.: 10/654,084

No extension of time is deemed necessary. Nonetheless, to the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Please recognize our Customer No. 53080
as our correspondence address.**